

# RSN33007F / RSN33007FT

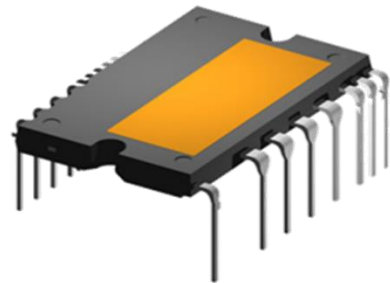


## Green Power Module (GPM)

### 650V 30A Inverter

#### Features

- 650V / 30A 3-Phase IGBT Inverter
- Low-Losses & Short-Circuit-Rated IGBTs
- Soft Reverse Recovery Diodes
- Built-In Bootstrap Diodes
- DBC Substrate
- Under-Voltage Lock-Out for high side and low side
- Short-Circuit Protection (SC)
- Over Temperature Protection (RSN3\*\*07FT only)
- LVIC Temperature Output (RSN3\*\*07F only)
- 3.3 V and 5V Input Logic Compatible : Active High
- Fault Signaling : LVIC UVLO and Short-circuit Protection
- Isolation Rating of 1500 Vrms/1 min
- UL 1557 Certified



#### Typical Applications

- Air-conditioner, Washing machine etc.
- Motor Control – Industrial Motor (AC 200 V Class)

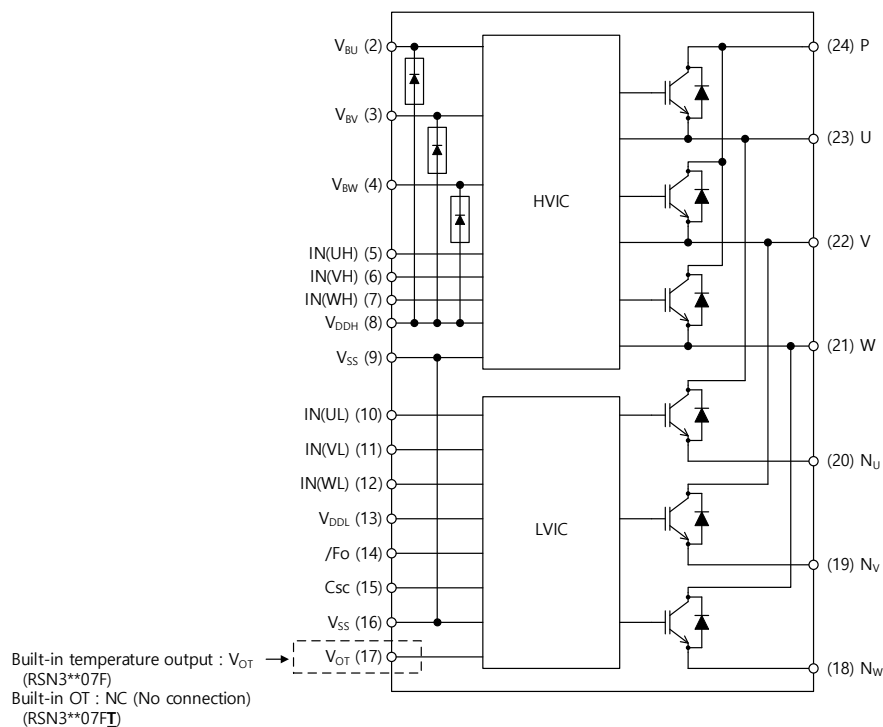


Figure 1. Internal Circuit

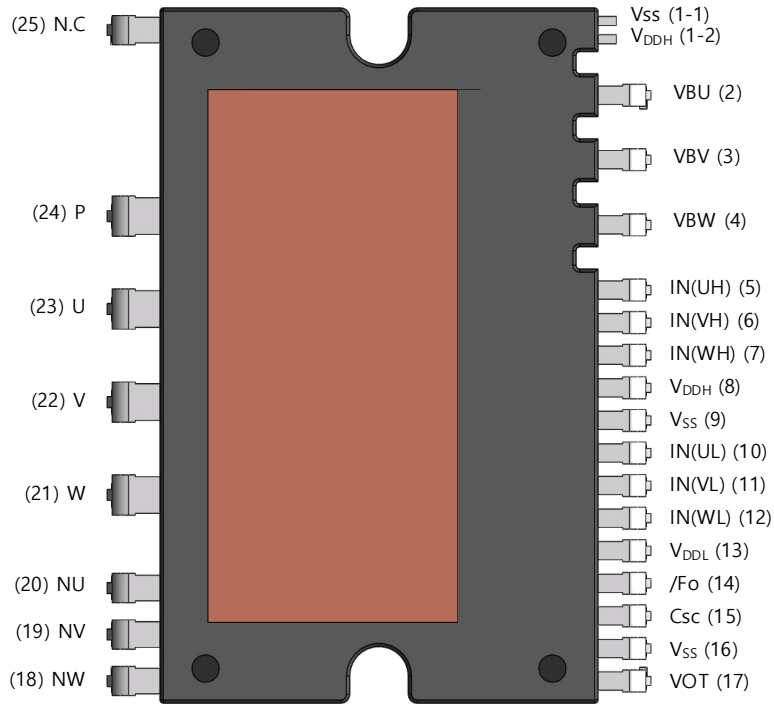


Figure 2. Pin Configuration – Top View

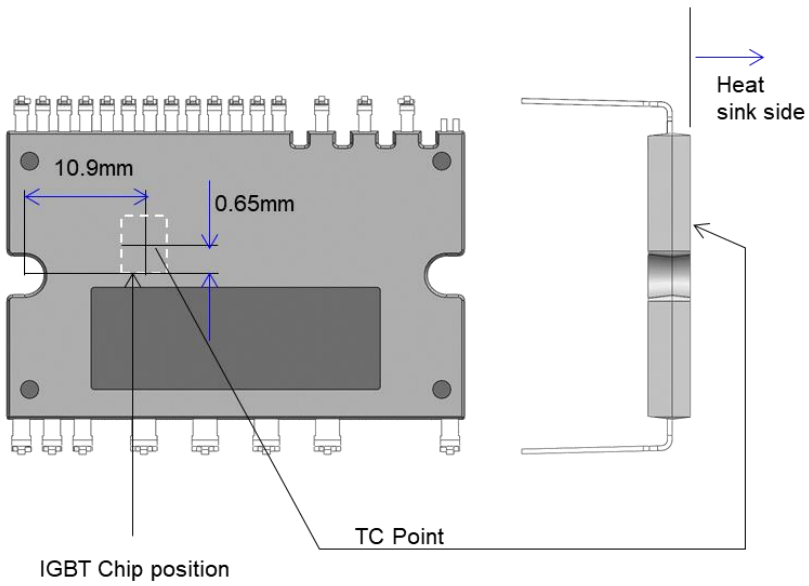


Figure 3. T<sub>c</sub> Measurement Point

**PIN DESCRIPTION**

| Pin Number | Pin Name         | Pin Description                                 |
|------------|------------------|---|
| (1-1)      | V <sub>SS</sub>  | Common Supply Ground, connected to HVIC         |
| (1-2)      | V <sub>DDH</sub> | High-Side Bias Voltage for U Phase IC           |
| 2          | V <sub>BU</sub>  | High-Side Bias Voltage for U Phase IGBT Driving |
| 3          | V <sub>BV</sub>  | High-Side Bias Voltage for V Phase IGBT Driving |
| 4          | V <sub>BW</sub>  | High-Side Bias Voltage for W Phase IGBT Driving |
| 5          | IN(UH)           | Signal Input for High-Side U Phase              |
| 6          | IN(VH)           | Signal Input for High-Side V Phase              |
| 7          | IN(WH)           | Signal Input for High-Side W Phase              |
| 8          | V <sub>DDH</sub> | High-Side Bias Voltage for U Phase IC           |
| 9          | V <sub>SS</sub>  | Common Supply Ground, connected to HVIC         |
| 10         | IN(UL)           | Signal Input for Low-Side U Phase               |
| 11         | IN(VL)           | Signal Input for Low-Side V Phase               |
| 12         | IN(WL)           | Signal Input for Low-Side W Phase               |
| 13         | V <sub>DDL</sub> | Low-Side Bias Voltage for IC and IGBTs Driving  |
| 14         | /FO              | Fault Output                                    |
| 15         | Csc              | Input for Current Protection                    |
| 16         | V <sub>SS</sub>  | Common Supply Ground, connected to LVIC         |
| 17         | V <sub>OT</sub>  | Voltage Output for LVIC Temperature             |
| 18         | N <sub>W</sub>   | Negative DC-Link Input for W Phase              |
| 19         | N <sub>V</sub>   | Negative DC-Link Input for V Phase              |
| 20         | N <sub>U</sub>   | Negative DC-Link Input for U Phase              |
| 21         | W                | Output for W Phase                              |
| 22         | V                | Output for V Phase                              |
| 23         | U                | Output for U Phase                              |
| 24         | P                | Positive DC-Link Input                          |
| 25         | NC               | Not Connected                                   |

## ABSOLUTE MAXIMUM RATINGS (T<sub>j</sub> = 25°C unless otherwise noted)

| Symbol | Rating | Conditions | Rating | Unit |
|--------|--------|------------|--------|------|
|--------|--------|------------|--------|------|

### INVERTER PART

|                        |                                    |   |         |    |
|------------------------|------------------------------------|---|---------|----|
| V <sub>PN</sub>        | Supply Voltage                     | Applied between P-N <sub>U</sub> , N <sub>V</sub> , N <sub>W</sub>          | 450     | V  |
| V <sub>PN(surge)</sub> | Supply Voltage (Surge)             | Applied between P-N <sub>U</sub> , N <sub>V</sub> , N <sub>W</sub>          | 500     | V  |
| V <sub>ces</sub>       | Collector-Emitter Voltage          |   | 650     | V  |
| I <sub>c</sub>         | Each IGBT Collector Current        | T <sub>c</sub> =25°C, T <sub>j</sub> ≤150°C                                 | 30      | A  |
| I <sub>cp</sub>        | Each IGBT Collector Current (Peak) | T <sub>c</sub> =25°C, T <sub>j</sub> ≤150°C, Under 1ms Pulse Width (Note 1) | 60      | A  |
| P <sub>c</sub>         | Collector Dissipation              | T <sub>c</sub> =25°C per One Chip (Note 1)                                  | 114     | W  |
| T <sub>j</sub>         | Operating Junction Temperature     |   | -40~150 | °C |

### CONTROL PART

|                 |                                |  |                           |    |
|-----------------|--------------------------------|--|---------------------------|----|
| V <sub>DD</sub> | Control Supply Voltage         | Applied between V <sub>DDH</sub> , V <sub>DDL</sub> -V <sub>SS</sub>   | 20                        | V  |
| V <sub>BS</sub> | High-Side Control Bias Voltage | Applied between V <sub>BU</sub> -V <sub>SU</sub> , V <sub>BV</sub> -V <sub>SV</sub> , V <sub>BW</sub> -V <sub>SW</sub> | 20                        | V  |
| V <sub>IN</sub> | Input Signal Voltage           | Applied between IN(UH), IN(VH), IN(WH), IN(UL), IN(VL), IN(WL) - V <sub>SS</sub>                                       | -0.5-V <sub>DD</sub> +0.5 | V  |
| V <sub>FO</sub> | Fault Output Supply Voltage    | Applied between F <sub>O</sub> -V <sub>SS</sub>  | -0.5-V <sub>DD</sub> +0.5 | V  |
| I <sub>FO</sub> | Fault Output Current           | Sink Current at F <sub>O</sub> pin   | 5                         | mA |
| V <sub>sc</sub> | Current Sensing Input Voltage  | Applied between C <sub>sc</sub> -V <sub>SS</sub>   | -0.5-V <sub>DD</sub> +0.5 | V  |
| T <sub>j</sub>  | Operating Junction Temperature |  | -40~150                   | °C |

### BOOSTSTRAP DIODE PART

|                  |                                    |  |         |    |
|------------------|------------------------------------|--|---------|----|
| V <sub>RRM</sub> | Maximum Repetitive Reverse Voltage |  | 650     | V  |
| T <sub>j</sub>   | Operating Junction Temperature     |  | -40~150 | °C |

### TOTAL SYSTEM

|                       |  |   |         |                  |
|-----------------------|--|---|---------|------------------|
| V <sub>PN(PROT)</sub> | Self-Protection Supply Voltage Limit (Short-Circuit Protection Capability) | V <sub>DD</sub> =V <sub>BS</sub> =13.5~16.5V, T <sub>j</sub> =150°C, Non-Repetitive, < 2 μs | 400     | V                |
| T <sub>c</sub>        | Module Case Operation Temperature  | See Figure 2  | -40~125 | °C               |
| T <sub>stg</sub>      | Storage Temperature  |   | -40~125 | °C               |
| V <sub>iso</sub>      | Isolation Voltage  | 60 Hz, Sinusoidal, AC 1 Minute, Connection Pins to Heat Sink Plate                          | 1500    | V <sub>rms</sub> |

1. These values had been made an acquisition by the calculation considered to design factor.

### THERMAL RESISTANCE

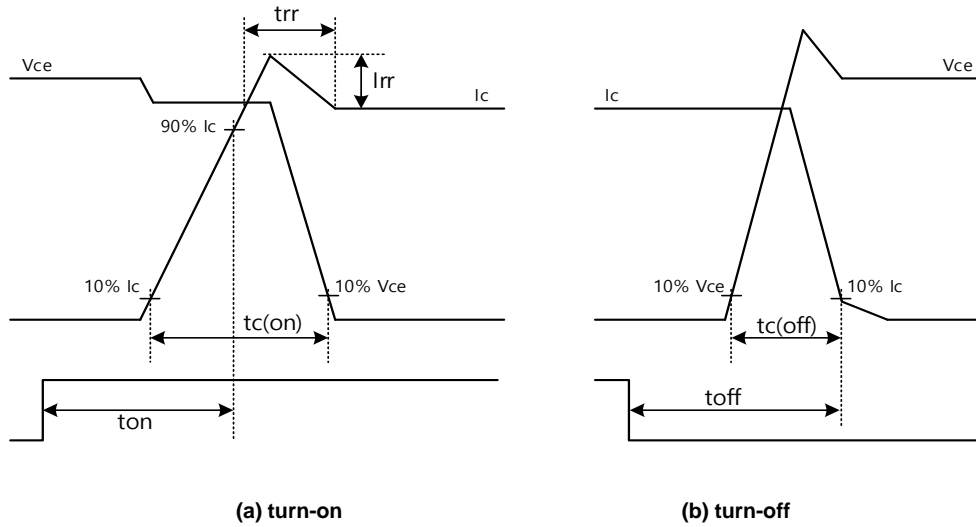
| Symbol                | Parameter                                    | Conditions                          | Min | Typ | Max | Unit |
|-----------------------|--|-------------------------------------|-----|-----|-----|------|
| R <sub>th(j-c)Q</sub> | Junction-to-Case Thermal Resistance (Note 2) | Inverter IGBT Part (per 1/6 module) | -   | -   | 1.1 | °C/W |
| R <sub>th(j-c)F</sub> |  | Inverter FWDi Part (per 1/6 module) | -   | -   | 1.8 | °C/W |

2. For the measurement point of case temperature (T<sub>c</sub>), please refer to Figure 2.

**ELECTRICAL CHARACTERISTICS** ( $T_j = 25^\circ\text{C}$  unless otherwise specified.)

| Symbol               | Parameter                            | Conditions   | Min  | Typ          | Max | Unit |               |               |
|----------------------|--------------------------------------|--|--|--------------|-----|------|---------------|---------------|
| <b>INVERTER PART</b> |                                      |  |  |              |     |      |               |               |
| $V_{CE(sat)}$        | Collector-Emitter Saturation Voltage | $V_{DD}=V_{BS}=15\text{V}$ $V_{IN}=5\text{V}$  | $I_c=30\text{A}$ , $T_j=25^\circ\text{C}$  | -            | 1.6 | 1.9  | V             |               |
| $V_F$                | FWDi Forward Voltage                 | $V_{IN}=0\text{V}$   | $I_c=-30\text{A}$ , $T_j=25^\circ\text{C}$   | -            | 1.6 | 2.1  | V             |               |
| HS                   | Switching Times                      | $V_{PN}=300\text{V}$ , $V_{DD}=15\text{V}$ , $I_c=30\text{A}$ $T_j=25^\circ\text{C}$<br>$V_{IN}=0\text{V} \leftrightarrow 5\text{V}$ , Inductive Load, See Figure 3 (Note 3) | $t_{on}$   | -            | 1.5 | -    | $\mu\text{s}$ |               |
|                      |                                      |  | $t_{c(on)}$  | -            | 0.2 | -    | $\mu\text{s}$ |               |
|                      |                                      |  | $t_{off}$  | -            | 1.5 | -    | $\mu\text{s}$ |               |
|                      |                                      |  | $t_{c(off)}$   | -            | 0.1 | -    | $\mu\text{s}$ |               |
|                      |                                      |  | $t_{rr}$   | -            | 0.1 | -    | $\mu\text{s}$ |               |
| LS                   | Switching Times                      |  | $V_{PN}=300\text{V}$ , $V_{DD}=15\text{V}$ , $I_c=30\text{A}$ $T_j=25^\circ\text{C}$<br>$V_{IN}=0\text{V} \leftrightarrow 5\text{V}$ , Inductive Load, See Figure 3 (Note 3) | $t_{on}$     | -   | 1.4  | -             | $\mu\text{s}$ |
|                      |                                      |  |  | $t_{c(on)}$  | -   | 0.2  | -             | $\mu\text{s}$ |
|                      |                                      |  |  | $t_{off}$    | -   | 1.4  | -             | $\mu\text{s}$ |
|                      |                                      |  |  | $t_{c(off)}$ | -   | 0.1  | -             | $\mu\text{s}$ |
|                      |                                      |  |  | $t_{rr}$     | -   | 0.1  | -             | $\mu\text{s}$ |
| $I_{ces}$            | Collector-Emitter Leakage Current    | $V_{ce} = V_{ces}$   |  | -            | -   | 1    | mA            |               |

3.  $t_{on}$  and  $t_{off}$  include the propagation delay of the internal drive IC.  $t_{c(on)}$  and  $t_{c(off)}$  are the switching times of IGBT under the given gate-driving condition internally. For the detailed information, please see Figure 3.



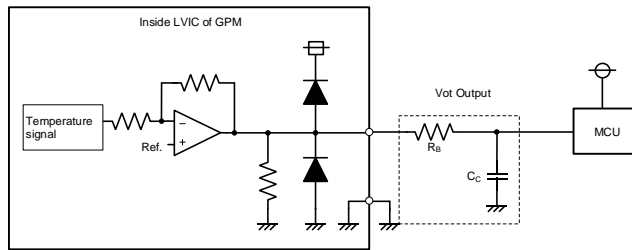
**Figure 3. Switching Time Definition**

| Symbol              | Parameter                         | Conditions   | Min  | Typ   | Max  | Unit  |    |
|---------------------|-----------------------------------|--|--|-------|------|-------|----|
| <b>CONTROL PART</b> |                                   |  |  |       |      |       |    |
| $I_{QDDH}$          | Quiescent $V_{DD}$ Supply Current | $V_{DDH}=15\text{V}$ ,<br>$V_{IN(UH,VH,WH)}=0\text{V}$                                     | $V_{DDH}-V_{SS}$                                       | -     | -    | 0.10  | mA |
| $I_{QDDL}$          |                                   | $V_{DDL}=15\text{V}$ ,<br>$V_{IN(UL,VL,WL)}=0\text{V}$                                     | $V_{DDL}-V_{SS}$                                       | -     | -    | 1.50  | mA |
| $I_{QBS}$           | Quiescent VBS Supply Current      | $V_{DD}=V_{BS}=15\text{V}$ ,<br>$V_{IN(UH,VH,WH)}=0\text{V}$                               | $V_{BU}-V_{SU}$ , $V_{BV}-V_{SV}$ ,<br>$V_{BW}-V_{SW}$ | -     | -    | 0.10  | mA |
| $V_{FOH}$           | Fault Output Voltage              | $V_{DD}=15\text{V}$ , $V_{sc}=0\text{V}$ ,<br>$V_{FO}$ Circuit: 10k $\Omega$ to 5V Pull-up | 4.90   | -     | -    | V     |    |
| $V_{FOL}$           |                                   | $V_{DD}=15\text{V}$ , $V_{sc}=1\text{V}$ , $I_{FO}=1\text{mA}$                             | -  | -     | 0.95 | V     |    |
| $V_{SC(ref)}$       | Short Circuit Trip Level          | $V_{DDH}=V_{DDL}=15\text{V}$   | $C_{sc}-V_{SS}$  | 0.455 | 0.48 | 0.505 | V  |

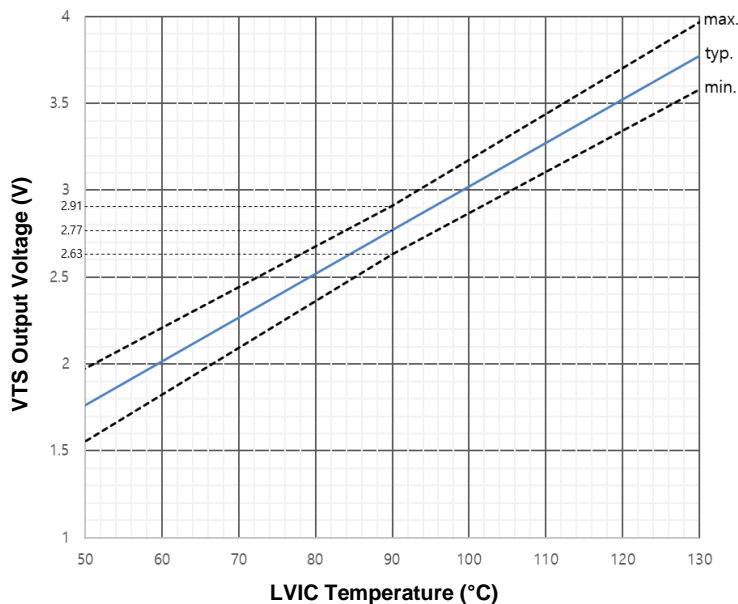
**ELECTRICAL CHARACTERISTICS** ( $T_j = 25^\circ\text{C}$  unless otherwise specified.) (continued)

| Symbol              | Parameter  | Conditions  | Min                      | Typ  | Max  | Unit |                  |
|---------------------|--|---|--------------------------|------|------|------|------------------|
| <b>CONTROL PART</b> |  |   |                          |      |      |      |                  |
| $U_{VDD}$           | Supply Circuit Under-Voltage Protection          | Detection Level   | 10.3                     | -    | 12.5 | V    |                  |
| $U_{VDDR}$          |  | Reset Level   | 10.8                     | -    | 13.0 | V    |                  |
| $U_{VBSD}$          |  | Detection Level   | 10.0                     | -    | 12.0 | V    |                  |
| $U_{VBSR}$          |  | Reset Level   | 10.5                     | -    | 12.5 | V    |                  |
| $V_{IN(ON)}$        | ON Threshold Voltage                             | Applied between $V_{IN(UH,VH,WH)}-V_{SS}$ , $V_{IN(UL,VL,WL)}-V_{SS}$             | -                        | -    | 2.6  | V    |                  |
| $V_{IN(OFF)}$       | OFF Threshold Voltage                            |   | 0.8                      | -    | -    | V    |                  |
| $OT_{trip}$         | Over temperature protection (RSN3**07FI)         | VDD=15V<br>Detect LVIC temperature  | Trip level               | 130  | 140  | 150  | $^\circ\text{C}$ |
| $OT_{hys}$          |  |   | Hysteresis of trip-reset | -    | 10   | -    | $^\circ\text{C}$ |
| $V_{OT}$            | Voltage Output for LVIC Temperature Sensing Unit | $V_{DDL}=15\text{V}$ , $T_{LVIC}=25^\circ\text{C}$<br>See Figure 4 and 5 (Note 5) | 0.88                     | 1.13 | 1.39 | V    |                  |
| $t_{FOD}$           | Fault-Out Pulse Width                            |   | 20                       | -    | -    | us   |                  |

- Short-circuit current protection functions only at the low-sides because the sense current is divided from main current at low-side IGBTs. Inserting the shunt resistor for monitoring the phase current at NU, NV, NW terminal, the trip level of the short-circuit current is changed.
- $T_{LVIC}$  is LVIC temperature and  $V_{OT}$  is only for sensing temperature of LVIC and cannot shutdown IGBTs automatically. The relationship between  $V_{OT}$  voltage output and LVIC temperature is described in Figure 5. It is recommended to add a ceramic capacitor of 10 nF or more between  $V_{OT}$  and  $V_{SS}$  (Signal Ground) to make the  $V_{OT}$  more stable. Refer to the application note for this products about usage of  $V_{OT}$ .



**Figure 4. Internal Block Diagram and Interface Circuit of  $V_{OT}$**



**Figure 5. Temperature Profile of  $V_{OT}$**

## ELECTRICAL CHARACTERISTICS ( $T_j = 25^\circ\text{C}$ unless otherwise specified.) (continued)

### BOOTSTRAP DIODE/RESISTOR PART

| Symbol | Parameter       | Conditions                                 |              | Min | Typ | Max | Unit |
|--------|-----------------|--|--------------|-----|-----|-----|------|
| $V_F$  | Forward Voltage | $I_F=0.1\text{A}$ , $T_j=25^\circ\text{C}$ | See Figure 6 | -   | 1.6 | -   | V    |

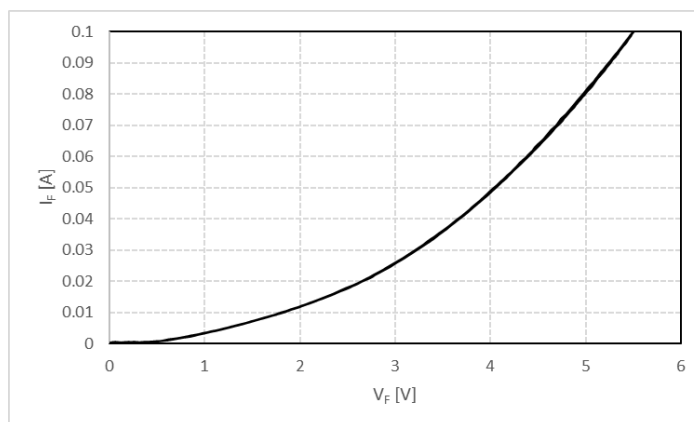


Figure 6. Characteristics of Bootstrap Diode

### RECOMMENDED OPERATING RANGES

| Symbol   | Parameter                                | Conditions  | Min  | Typ  | Max  | Unit             |
|--|--|---|------|------|------|------------------|
| $V_{PN}$                                       | Supply Voltage                           | Applied between P-N <sub>U</sub> , N <sub>V</sub> , N <sub>W</sub>  | -    | 300  | 400  | V                |
| $V_{DD}$                                       | Control Supply Voltage                   | Applied between $V_{DDH}-V_{SS}$ , $V_{DDL}-V_{SS}$   | 13.5 | 15.0 | 16.5 | V                |
| $V_{BS}$                                       | High-Side Control Bias Voltage           | Applied between $V_{BU}-V_{SU}$ , $V_{BV}-V_{SV}$ , $V_{BW}-V_{SW}$                                       | 13.0 | 15.0 | 18.5 | V                |
| $\frac{dV_{DD}}{dt}$ ,<br>$\frac{dV_{BS}}{dt}$ | Control Supply Variation                 |   | -1   | -    | +1   | V/ $\mu\text{s}$ |
| $t_{\text{dead}}$                              | Blanking Time for Preventing Arm - Short | For Each Input Signal   | 1.0  | -    | -    | $\mu\text{s}$    |
| $f_{\text{PWM}}$                               | PWM Input Signal                         | $-40^\circ\text{C} \leq T_C \leq 125^\circ\text{C}$ , $-40^\circ\text{C} \leq T_j \leq 150^\circ\text{C}$ | -    | -    | 20   | kHz              |
| PWIN(ON)                                       | Minimum Input Pulse Width                | (Note 6)  | 0.7  | -    | -    | $\mu\text{s}$    |
| PWIN(OFF)                                      |  |   | 0.7  | -    | -    |                  |
| $T_j$  | Junction Temperature                     |   | -40  | -    | +150 | $^\circ\text{C}$ |

- This product might not make output response if input pulse width is less than the recommended value.
- RC coupling at each input might change depending on the PWM control scheme used in the application and the wiring impedance of the application's printed circuit board. The input signal section integrates 5 k $\Omega$  (typ.) pull-down resistor. Therefore, when using an external filtering resistor, please pay attention to the signal voltage drop at input terminal.

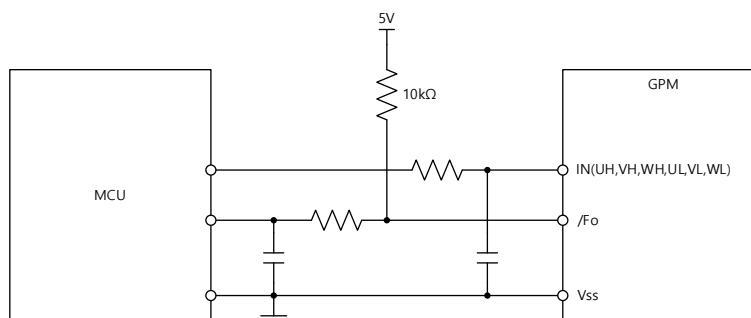


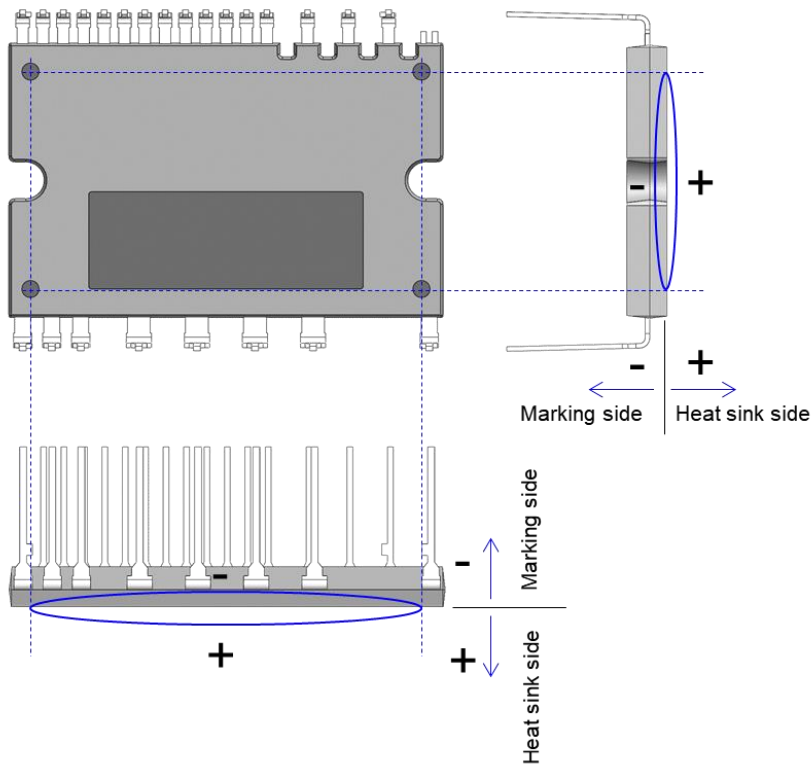
Figure 7. Recommended MCU I/O Interface Circuit

**PACKAGE MARKING AND ORDERING INFORMATION**

| Device     | Device Marking | Package  | Shipping   |
|------------|----------------|----------|------------|
| RSN33007F  | RSN33007F      | GPM24-AA | 480 ea/box |
| RSN33007FT | RSN33007FT     | GPM24-AA | 480 ea/box |

**MECHANICAL CHARACTERISTICS AND RATINGS**

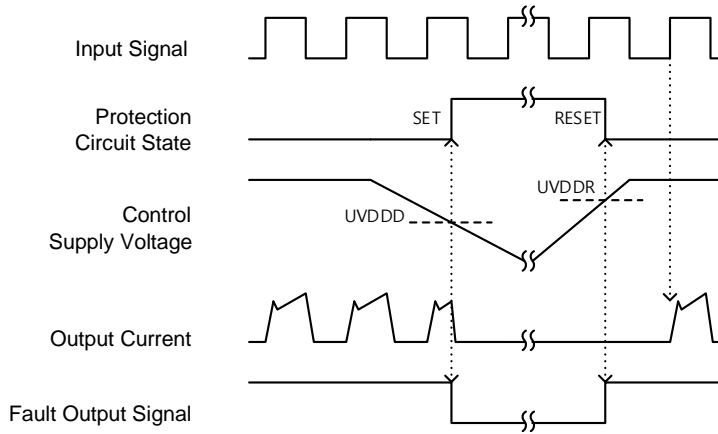
| Parameter                 | Conditions  | Min  | Typ  | Max  | Unit  |
|---------------------------|---|------|------|------|-------|
| Device Flatness           | See Figure 8  | -50  | -    | 120  | μm    |
| Mounting Torque           | Mounting Screw: M3<br>Recommended 1.18 N·m                                | 0.59 | 0.69 | 0.78 | N·m   |
| Terminal Pulling Strength | Control terminal : Load 5N<br>Power terminal : Load 10N                   | 10   | -    | -    | s     |
| Terminal Bending Strength | Control terminal : Load 5N<br>Power terminal : Load 10N<br>90 degree Bend | 2    | -    | -    | times |
| Weight                    |   | -    | 9.6  | -    | g     |



**Figure 8. Flatness Measurement Position**

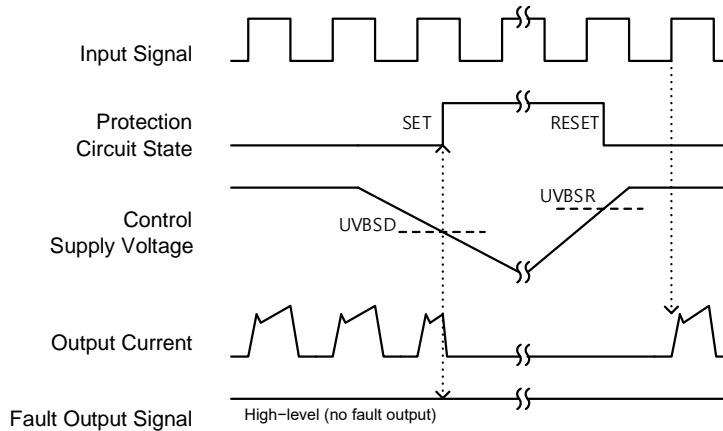


## TIME CHARTS OF GPMs PROTECTIVE FUNCTION



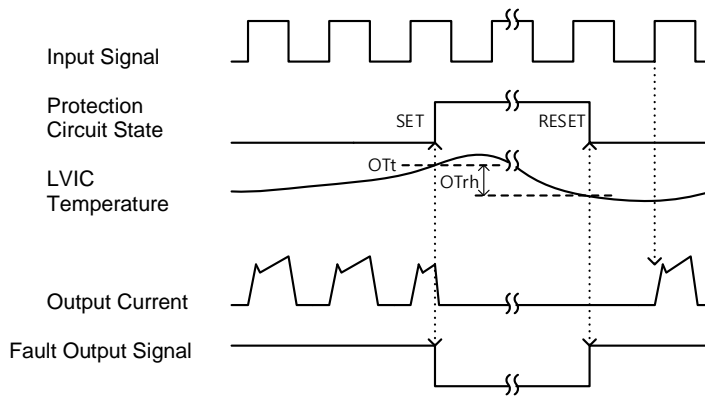
- 1: Normal operation: IGBT ON and carrying current.
- 2: Under-voltage detection (UVDDD).
- 3: All low-side IGBT OFF in spite of control input condition.
- 4: Fault output operation starts with a fixed pulse width.
- 5: Under-voltage reset (UVDDR).
- 6: Normal operation: IGBT ON and carrying current by triggering next signal from LOW to HIGH.

**Figure 9. Under-voltage Protection (Low-side)**



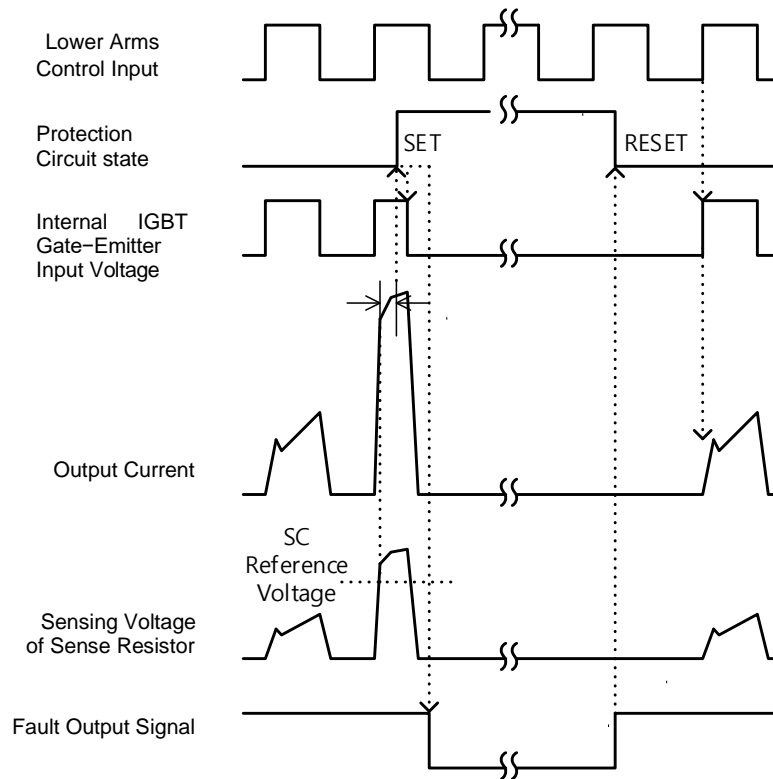
- 1: Normal operation: IGBT ON and carrying current.
- 2: Under-voltage detection (UVBSD).
- 3: IGBT OFF in spite of control input condition, but there is no fault output signal.
- 4: Under-voltage reset (UVBSR).
- 5: Normal operation: IGBT ON and carrying current by triggering next signal from LOW to HIGH.

**Figure 10. Under-voltage Protection (High-side)**



- 1: Normal operation: IGBT ON and carrying current.
- 2: LVIC temperature exceed OT trip level ( $OT_t$ ).
- 3: All low-side IGBTs OFF in spite of control input condition.
- 4: Fault output operation starts with a fixed pulse width (min. 20us).
- 5: LVIC temperature drop to OT reset level.
- 6: Normal operation: IGBT ON and carrying current by triggering next signal from LOW to HIGH.

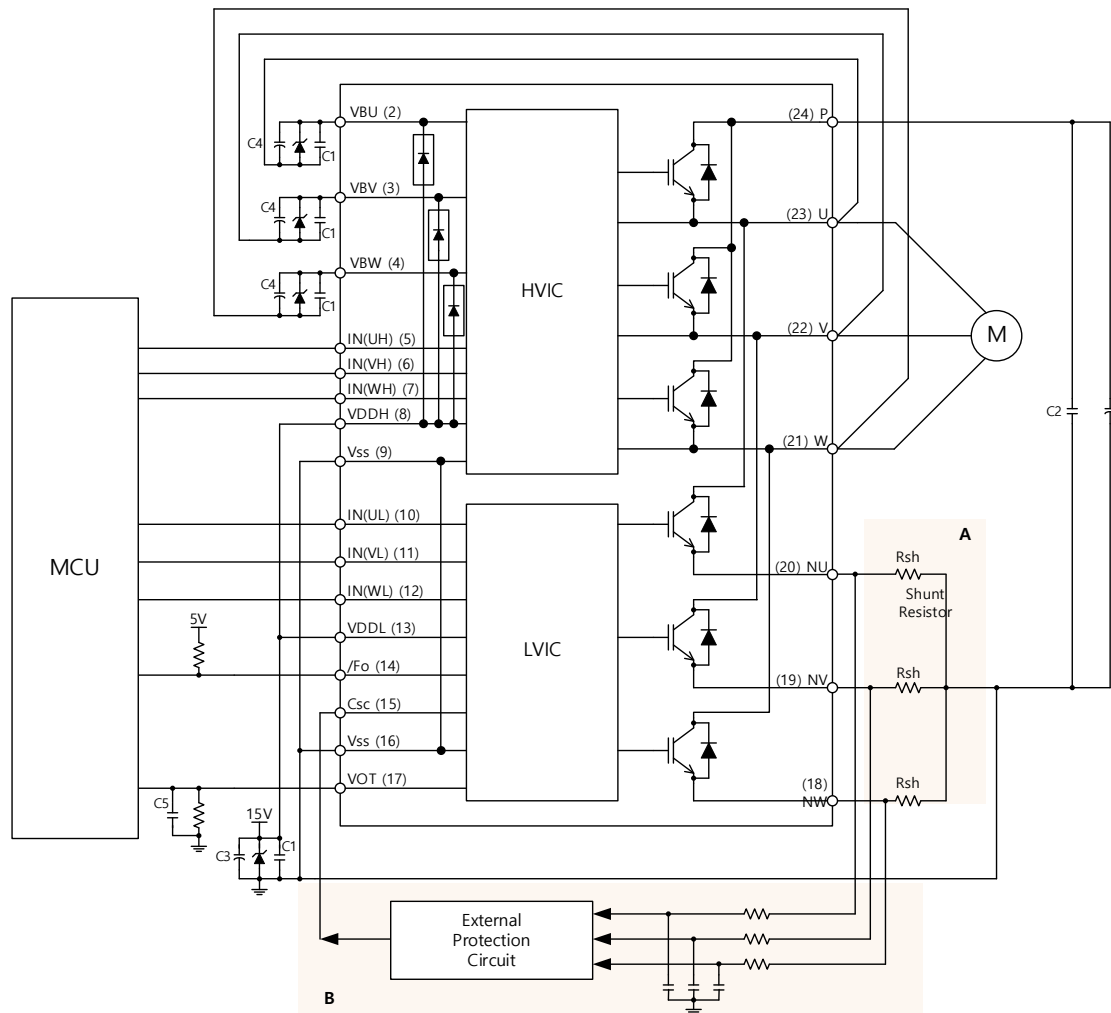
**Figure 11. Over-temperature Protection (RSN3\*\*07FI only)**



(With the external sense resistance and RC filter connection)

- 1: Normal operation: IGBT ON and carrying current.
- 2: Short-circuit current detection (SC trigger).
- 3: All low-side IGBTs gate are hard interrupted.
- 4: All low-side IGBTs turn OFF.
- 5: Fault output operation starts with a fixed pulse width (min. 20us).
- 6: Input HIGH – IGBT ON state, but during the active period of fault output, the IGBT doesn't turn ON.
- 7: Fault output operation finishes, but IGBT doesn't turn on until triggering the next signal from LOW to HIGH.
- 8: Normal operation: IGBT ON and carrying current.

**Figure 12. Short-circuit Current Protection (Low-side Operation Only)**



**Figure 13. Typical Application Circuit**

**NOTES:**

8. To avoid malfunction, the wiring of each input should be as short as possible (less than 2~3 cm).
9. Fault output is an open-drain type. This signal line should be pulled up to the positive side of the MCU or control power supply with a resistor that makes  $I_{FO}$  up to 1 mA. Please refer to Figure 7.
10. Input signal is active-HIGH type. There is a 5 k $\Omega$  resistor inside the IC to pull-down each input signal line to GND. RC coupling circuits should be adopted for the prevention of input signal oscillation. RC time constant should be selected in the range 50~150 ns (recommended R = 100 $\Omega$ , C = 1 nF). Please refer to Figure 7.
11. Each wiring pattern inductance of point A should be minimized (recommend less than 10 nH). Use the shunt resistor Rsh of surface mounted (SMD) type to reduce wiring inductance.
12. To insert the shunt resistor to measure each phase current at  $N_U$ ,  $N_V$ ,  $N_W$  terminal, it makes to change the trip level  $I_{SC}$  about the short-circuit current.
13. To prevent errors of the protection function, the wiring of point B should be as short as possible.
14. For stable protection function, use the sense resistor Rsh with resistance variation within 1% and low inductance value.
15. In the short-circuit protection circuit, select the RC time constant of protection circuit in the range 2.0~2.5  $\mu$ s.
16. Each capacitor C1 should be mounted as close to the pins of the GPM product as possible.
17. To prevent surge destruction, the wiring between the smoothing capacitor C2 and the P & GND pins should be as short as possible. The use of a high-frequency non-inductive capacitor of around 0.1~0.22  $\mu$ F between the P & GND pins is recommended.
18. Relays are used in most systems of electrical equipment in industrial application. In these cases, there should be sufficient distance between the MCU and the relays.
19. The Zener diode or transient voltage suppressor should be adopted for the protection of ICs from the surge destruction between each pair of control supply terminals (recommended Zener diode is 22V/1W, which has the lower Zener impedance characteristic than about 15  $\Omega$ ).
20. C3 of around seven times larger than bootstrap capacitor C4 is recommended.
21. Please choose the electrolytic capacitor with good temperature characteristic in C4. Choose 0.1~0.2 $\mu$ F R-category ceramic capacitors with good temperature and frequency characteristics in C1.
22. For stable analog voltage output for LVIC temperature, please use ceramic capacitor for C5.

